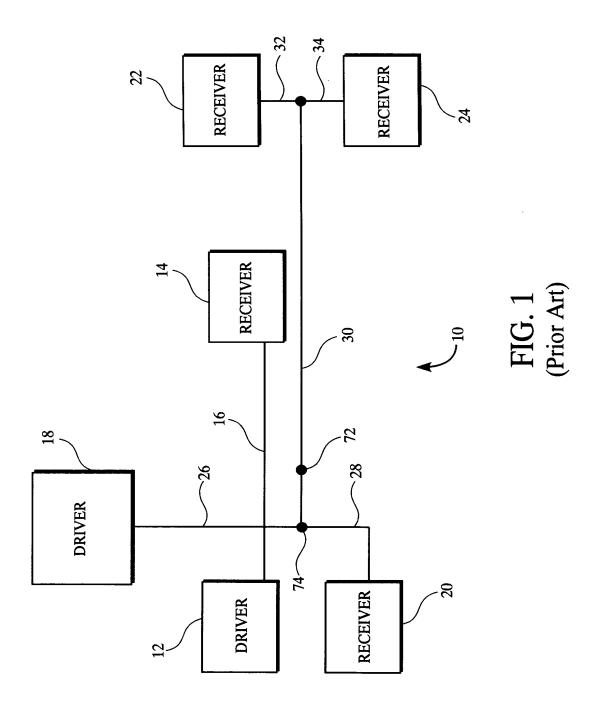
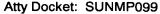
Title: METHOD FOR REDUCING NOISE IN INTEGRATED CIRCUIT LAYOUTS

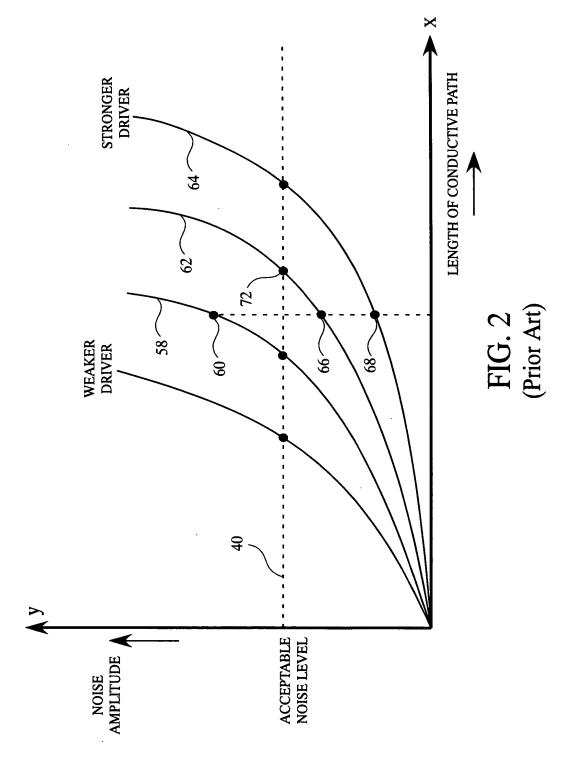
Inventors: Massimo Sutera et al. Application No: 09/430,350 Atty Docket: SUNMP099











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